

08/691822

Class Subclass
CLASSIFICATION

BEST AVAILABLE COPY **ABANDONED**

UTILITY SERIAL NUMBER	08/691822	PATENT DATE		PATENT NUMBER	
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SERIAL NUMBER	08/691822	FILING DATE	08/05/96	CLASS	327	SUBCLASS	534	GROUP ART UNIT	2504	EXAMINER	Cunningham
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APPLICANT
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CONTINUING DATA*****
VERIFIED THIS APPLN IS A CON OF 08/596,423 02/02/96 **ABN**
TDC WHICH IS A CON OF 08/376,347 01/23/95 **ABN**

FOREIGN/PCT APPLICATIONS*****
VERIFIED REPUBLIC OF KOREA 1994-1124 01/21/94
TDC

Foreign priority claimed	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no	AS FILED	STATE OR COUNTRY	SHEETS DRWGS.	TOTAL CLAIMS	INDEP CLAIMS	FILING FEE RECEIVED	ATTORNEY'S DOCKET NO.
USC 119 conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no		KRX	3	6	3	\$750.00	224955/P3334
Examined and Acknowledged	Examiner's Initials							

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SUBSTRATE BIAS GENERATOR IN SEMICONDUCTOR MEMORY DEVICE

U.S. DEPT. OF COMM./PAT. & TM—PTO-436L (Rev.12-94)

PARTS OF APPLICATION FILED SEPARATELY			Applications Examiner		
NOTICE OF ALLOWANCE MAILED			CLAIMS ALLOWED		
Amount Due: Date Paid:			Total Claims		Print Claim
			6		
Label Area			DRAWING		
			Sheets Drwg	Figs Drwg	Print Fig
Primary Examiner			ISSUE BATCH NUMBER		
			PREPARED FOR ISSUE		
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